

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**In re Patent Application of**

**ONO et al.**

**Application Number: To be Assigned**

**Filed: Concurrently herewith**

**For: SEMICONDUCTOR INTEGRATED CIRCUIT  
DEVICE**

**Attorney Docket No. NITT.0156**

**Art Unit 2816**

**Honorable Assistant Commissioner  
for Patents  
Washington, D.C. 20231**

**INFORMATION DISCLOSURE STATEMENT**

The above-referenced application is a Continuation of U.S. Serial No. 10/284,207 filed October 31, 2002. It includes the same disclosure as U.S. patent application Serial No. 10/284,207.

It is understood that the listed references will be considered in the examination of the application and that no separate copies of the same prior art are required to be provided since they were previously cited or transmitted in the foregoing prior application, pursuant to 37 CFR § 1.98(d). Form(s) PTO 1449 is enclosed listing references cited by the Examining Attorney and submitted by applicant in the prior applications.

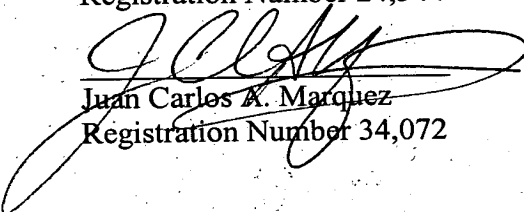
This Information Disclosure Statement is submitted with the above-captioned U.S. Continuation application. Accordingly, no fee is due or payable at this time.

The Examiner is requested to acknowledge consideration of the information provided in this paper in accordance with prescribed procedures.

Please charge any additional fees or credit any overpayments in connection with this paper to Deposit Account No. 08-1480.

Respectfully submitted,

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| Form PTO 1449<br><br>U.S. Department of Commerce<br>Patent and Trademark Office<br><br>Information Disclosure Statement by Applicant | ATTY. DOCKET NUMBER<br>NITT.0156     | SERIAL NUMBER<br>To Be Assigned |
|  | APPLICANT<br>ONO et al.              |                                 |
|  | FILING DATE<br>Concurrently Herewith | GROUP                           |

**U.S. Patent Documents**

| Examiner Initial | Cited by Examiner in Parent | DOCUMENT NUMBER | DATE  | NAME           | CLASS | SUBCLASS | FILING DATE |
|------------------|-----------------------------|-----------------|-------|----------------|-------|----------|-------------|
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**Foreign Patent Documents**

| Examiner Initial | Filed by Applicant in parent | DOCUMENT NUMBER | FILING DATE | COUNTRY | CLASS | SUB-CLASS | TRANSLATION |    |
|------------------|------------------------------|-----------------|-------------|---------|-------|-----------|-------------|----|
|                  |                              |                 |             |         |       |           | YES         | NO |
|                  | X                            | 2001-156261A    | 04/18/2000  | Japan   |       |           | Abstract    | X  |
|                  |                              |                 |             |         |       |           |             |    |
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|                  |                              |                 |             |         |       |           |             |    |
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**Other Documents (Including Author, Title, Date Pertinent Pages, Etc.)**

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